METHODS AND DEVICES FOR OPTIMIZING POWER CONSUMPTION OF TRIP UNITS CONTAINING MICROPROCESSORS

Abstract of Disclosure

A method is provided for optimizing the power consumption of a trip unit. The method comprises sensing a first output from a power system, the first output including a first current and a current-sensing signal; supplying a power supply with only the first current, or with a second current alone or in combination with the first current; powering a microprocessor from the power supply; inputting a second output to the microprocessor indicative of whether the power supply is receiving the second current; operating the microprocessor at a first state when the second output indicates the power supply is not receiving the second current; and operating the microprocessor at a second state when the second output indicates the power supply is receiving the second current alone or in combination with the first current.

Figures